Strategy and Synergy for Security

SOCIETY FOR ELECTRONIC TRANSACTIONS AND SECURITY [SETS]

CIT Campus, MGR Knowledge City, Taramani, Chennai – 600 113, India.

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Society for Electronic Transactions and Security [SETS] is a Society under Societies Registration Act, XXI of 1860, dedicated to carry-out Research and Development in the field of Information Security focusing on the key verticals, namely, Cryptology and Computing, Hardware Security, Quantum Security and Network Security.

SETS invites applications from citizens of India for filling up the following positions for a Research & Development project in the area of Cybersecurity.:

1. **Project Scientist (Consultant Mode) - 1 Position (Hardware)**

2. **Project Scientist (Consultant Mode) - 1 Position (Software)**

3. **Project Associate - 3 Positions (Hardware)**

4. **Project Associate - 2 Positions (Software)**

5. **JRF/SRF – 1 Position (Hardware)**

The descriptions of positions, detailed qualification requirements and salary are given below:

1. **Project Scientist (Consultant Mode) (Hardware)**

Name of the Post	Project Scientist (Consultant Mode) (Hardware)
Number of Posts	One
Age Limit	Not more than 35 years as on 10.01.2023
Qualification	First Class M.Tech /M.E (Computer Science/ Information Security/Cyber Security/Network Security/ Embedded Systems/ Communication Engineering)
Desirable Qualification	PhD in Embedded Systems/Electronics / Communication Engineering / Computer Science/ Cyber security/Network Security/Information Security /IoT
Areas of Skill sets/ Knowledge required	a) Linux, Windows Operating System and Kernel programming b) Programming Experience in C/C++ and Python c) Knowledge of assembly language (x86) d) Knowledge of Firmware development and testing e) Hands-on Experience in Hardware Emulation tools f) Hands-on experience in Networking tools and Hardware debugging tools with JTAG/UART g) Knowledge on Firmware Security Testing (FSTM) h) Certification: (At least one certification is preferred) i) CEH, CompTIA CySA, OSCP
Experience	5 Years of R&D Experience in Cyber Security in the area of VLSI/Embedded Systems/IoT/Wireless Technology/ Firmware Equivalent work experience in lieu of higher degree: - Engineering Doctorates: Four years
Remuneration	Consolidated salary of Rs.80,000 per month, additional increments may be given based on skill sets and relevant experience of candidate. (based on performance of the first year 5-8% increment may be considered)

2. Project Scientist (Consultant Mode) (Software)

Name of the Post	Project Scientist (Consultant Mode) (Software)
Number of Posts	One
Age Limit	Not more than 35 years as on 10.01.2023
Qualification	First Class M.Tech /M.E / MS by Research (Computer Science / Electronics and Communication/Electrical and Engineering/ Information Technology/ Information Security/Cyber security)
Desirable Qualification	PhD in Computer Science / Electronics and Communication/Electrical and Engineering/ Information Technology/Information Security/Cyber security
Areas of skill sets/Knowledge required	a) R&D of Cybersecurity solutions b) Expertise in the areas of Certifying Authority (CA), Cryptology and Blockchain c) Programming Experience in C, C++, Java, Go and Python d) Knowledge of Open SSL, Blockchain Technologies, Hyperledger Fabric/Sawtooth
Experience	5 Years of experience in R&D Projects in the area of Cyber Security, PKI, Cryptology, Blockchain and Network Security Equivalent work experience in lieu of higher degree: - Engineering Doctorates: Four years
Consolidated salary	Consolidated salary of Rs. 80,000 per month, additional increments may be given based on skill sets and relevant experience of candidate. (based on performance of the first year 5-8% increment may be considered)

3. Project Associate (Hardware)

Name of the Post	Project Associate (Hardware)
Number of Posts	Three
Age Limit	Not more than 35 years as on 10.01.2023
Qualification	First Class B.E / B.Tech ECE/EEE/E&I/CSE/IT/ICT
Desirable Qualification	First Class M.Tech /M.E / MS by Research (Embedded Systems/Communication Systems/VLSI Design/ Electronic Engineering /Radio Frequency &Microwave Engineering/Wireless Communication/ Computer Science/ Information Security/Cyber security/Network Security)
Areas of Skill sets/ Knowledge required	a) Linux, Windows Operating Systems and kernel programming b) Programming & Scripting Language Experience in C, C++ and Python c) Knowledge of assembly language (x86) d) Hands-on Experience in Hardware Emulation and Debugging tools (or) a) Hands-on experience in RF and Microwave Measurements, wireless communication and protocol analysis, EMI/EMC b) Knowledge of Microwave, Basic microwave theory, Smith chart applications to RF (or) a) Knowledge of FPGA implementation flow and Digital Electronics b) Hands-on experience in Xilinx Vivado tools, simulation using MATLAB / ModelSim and FPGA realization using VHDL/Verilog programming. c) Knowledge of Printed Circuit Board and its components

Experience	3 to 4 Years of Relevant Experience
	Equivalent work experience in lieu of higher degree: - Engineering Post-Graduates: Two years - Engineering Doctorates: Four years
Remuneration	Consolidated salary would be Rs.50,000 per month, additional increment may be given based on the skill sets and relevant experience of candidate. (based on performance of the first year 5-8% increment may be considered)

4. Project Associate (Software)

Name of the Post	Project Associate (Software)
Number of Posts	Two
Age Limit	Not more than 35 years as on 10.01.2023
Qualification	First Class B.E / B.Tech CSE/IT/ICT /ECE/EEE/E&I
Desirable Qualification	First Class M.Tech /M.E / MS by Research (Computer Science/Information Technology/Information Security/Cyber Security/ Network Security)
Areas of Skill sets/ Knowledge required	a) Programming Experience in C, C++ and Python b) Deep Learning/ Machine Learning Tools c) Knowledge of Open SSL, Blockchain Technologies, Hyperledger Fabric
Experience	0 to 2 Years of Experience in Research & Development Equivalent work experience in lieu of higher degree: Engineering Post-Graduates: Two years
Remuneration	Consolidated salary would be 30,000- 40,000/- per month commensurate with qualification, skill sets and relevant experience of candidate. (based on performance of the first year 5-8% increment may be considered)

5. JRF / SRF (Hardware)

Name of the Post	JRF / SRF (Hardware)
Number of Posts	One
Age Limit	Not more than 35 years as on 10.01.2023
Qualification	First Class M.Tech /M.E / MS by Research (Embedded Systems/Communication Systems/VLSI Design/ Electronic Engineering /Wireless Communication/ Computer Science/ Information Security/Cyber security/Network Security)
Areas of Skill sets/ Knowledge required	a) Knowledge of FPGA implementation flow and Digital Electronics b) Hands-on experience in Xilinx Vivado tools, simulation using MATLAB / ModelSim and FPGA realization using VHDL/Verilog programming.
Experience	0 to 2 Years of Relevant Experience
Remuneration	Salary as per DST Norms (Rs.31,000 – 35,000 + 24%HRA per month), additional increment may be given based on the skill sets and relevant experience of candidate. The appointment is initially for six months and extendable till the duration of the project.

The positions of Project Scientist (Consultant Mode) & Project Associates as proposed is purely temporary and would be filled on a contract basis with a consolidated salary underproject mode. The duration of the assignment is initially for a period of One Year and would be extended further based on the need of the project and performance. There is no scope of continuation/regularization/absorption under any circumstances.

Application Procedure:

1. **Only applications received via Google Form will be considered. The Candidate should choose the appropriate post to apply ("Project Scientist (Consultant Mode)/Project Scientist (Consultant Mode)/Project Associate - (Hardware)/Project Associate - (Software) / JRF/SRF"). Please find below the link for the Google form:**

Name of the Post	Google Form Link
1. Project Scientist (Consultant Mode) (Hardware)	https://forms.gle/Aa5eXQqdTeeTo7kDA
2. Project Scientist (Consultant Mode) (Software)	https://forms.gle/3Dcy6Xb7qBuza9kh8
3. Project Associate (Hardware)	https://forms.gle/TmtJt5pTKAGqk1i98
4. Project Associate (Software)	https://forms.gle/JMY7X8xujeNu9Njt5
5. JRF/SRF (Hardware)	https://forms.gle/jUPixfyDYsrn8N388

2. The candidate is requested to attach the following documents: -
   a) Scanned copy of Personal Particulars form (pdf format) duly filled and signed. The Personal Particulars form is available at SETS homepage https://setsindia.in/careers.
   b) Scanned copies of academic certificates (Single PDF document)
   c) Scanned copies of Experience certificates (Single PDF document)
3. The last date for uploading applications via Google Form is 10.01.2023
4. Shortlisted candidates would be required to attend a Written Test and/or Interview at SETS,Chennai. The date and time for this would be intimated to shortlisted candidates by Email.

Terms and Conditions:

1. The shortlisted candidates would be required to bring all their original testimonials for verification on the interview day.
2. No TA/DA will be given to candidates appearing for interview.
3. The prescribed qualifications are minimum and mere possession of the same does not entitle the candidate to be called for the written test or interview. The decision of the Executive Director of SETS in all matters relating to eligibility, acceptance or rejection ofthe applications, cancellation of advertisement and filling up or not filling up of post will be final and no inquiry or correspondence will be entertained in this matter.

❖ In case of any queries/issues related to uploading the form may please be intimated via emailid: hrd2022-05-proj@setsindia.net

Executive Director